Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of the Claims

1. (Currently Amended) A method for adjusting the frequency of a local oscillator, comprising the steps of:

   receiving a television signal at a television signal receiving device;

   determining a first bit rate multiplier value for adjusting the frequency of a local oscillator from said television signal; and

   replacing a second bit rate multiplier value for adjusting the frequency of a local oscillator, stored in a memory of said television signal receiving device, with said first bit rate multiplier value for adjusting the frequency of a local oscillator, determined from said television signal.

2. (Original) The method of claim 1 wherein the step of replacing said second value with said first value is performed only in response to said first value being different than said second value.

3. (Original) The method of claim 1 wherein the step of replacing said second value with said first value is performed only in response to said first value being greater than 15 parts per million different than said second value.

4. (Original) The method of claim 1 wherein the step of replacing said second value with said first value is performed only in response to said first value being greater than 0.0015% different than said second value.

5. (Original) The method of claim 1 wherein the step of determining a first value from said television signal comprises the steps of:

   receiving a first data packet;

   receiving a second data packet;

   determining a difference between the information received in said first data packet and the information in said second data packet; and

   determining said first value in response to said difference.

6.   (Original) The method of claim 5 wherein the information received in said first data packet and said second data packet are time references.

7.   (Currently Amended) An <u>television signal receiving</u> apparatus comprising:

           a memory for storing a first oscillator <u>bit rate multiplier</u> parameter;

           an input for receiving a television signal comprising time reference data; and

           a processing means for determining a second oscillator <u>bit rate multiplier</u> parameter in response to said time reference data and storing said second oscillator <u>bit rate multiplier</u> parameter in said memory.

8.   (Original) The apparatus of claim 7 wherein said processor replaces said first oscillator parameter with said second oscillator parameter in response to said second oscillator parameter being different than said first oscillator parameter.

9.   (Original) The apparatus of claim 7 wherein said processor replaces said first oscillator parameter with said second oscillator parameter in response to when said second oscillator parameter being greater than 0.0015% different than said first oscillator parameter.

10.   (Original) The apparatus of claim 7 wherein said processor replaces said first oscillator parameter with said second oscillator parameter in response to said second oscillator parameter being greater than 15 parts per million different than said first oscillator parameter.

11.   (Original) The apparatus of claim 7 wherein said first oscillator parameter and said second oscillator parameter is a bit rate multiplier value.

12.   (Currently Amended) A method for updating a digital video signal processor parameter comprising a processing means for:

           extracting a first time stamp from a first data packet,

           extracting a second time stamp from a second data packet;

           determining the time interval between the first time stamp and the second time stamp;

           calculating a video signal processor <u>bit rate multiplier</u> parameter based on said time interval;

           replacing a stored video signal processor <u>bit rate multiplier</u> parameter with said video signal processor parameter.

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13.   (Original) The method of claim 12 wherein said stored video signal processor parameter is replaced with said video signal processor parameter only in response to said video signal processor parameter being different than said stored video signal processor parameter.

14.   (Original) The apparatus of claim 12 wherein said stored video signal processor parameter is replaced with said video signal processor parameter only in response to said video signal processor parameter being greater than 0.0015% different than said stored video signal processor parameter.

15.   (Original) The apparatus of claim 12 wherein said stored video signal processor parameter is replaced with said video signal processor parameter only when said video signal processor parameter is greater than 15 parts per million different than said stored video signal processor parameter.